



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in the patent application of

Makabe, et al.

Serial No.: 10/606,294

Group Art Unit: Unknown

Filed: June 26, 2003

Examiner: Unknown

For: HIGH-PERFORMANCE MOS TRANSISTOR OF LDD STRUCTURE AND
MANUFACTURING METHOD OF THE SAME

Commissioner for Patents
Alexandria, VA 22313-1450

REQUEST FOR RECORDATION OF ASSIGNMENT

Sir:

The undersigned respectfully requests a Recordation of Assignment for the above-identified patent application. The Assignment was filed with the original application of September 30, 2003.

Therefore, Applicant respectfully requests that the Examiner review and record the Assignment submitted on September 30, 2003.

Respectfully submitted,

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Date: 9/30/04

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